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## APPLICANTS

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\*\* CONTINUING DATA *N* *VS* .....\*\* FOREIGN APPLICATIONS *N* *VS* .....IF REQUIRED, FOREIGN FILING LICENSE  
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\*\* 11/18/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>Vincent S. [Signature]</i>	Initials <i>VS</i>		

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## TITLE

Method of clock driven cell placement and clock tree synthesis for integrated circuit design

FILING FEE  RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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